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Class Work:	50
Exam:	100
Total:	150
Duration of Exam: 3 Hrs.	

Unit-1: Introduction: Introduction to Operating System Concepts (including Multitasking, multiprogramming, multi user, Multithreading etc)., Types of Operating Systems: Batch operating system, Time-sharing systems, Distributed OS, Network OS, Real Time OS; Various Operating system services, architecture, System programs and calls.

Unit-2: Process Management: Process concept, process scheduling, operation on processes; CPU scheduling, scheduling criteria, scheduling algorithms -First Come First Serve (FCFS), Shortest-Job-First (SJF), Priority Scheduling, Round Robin(RR), Multilevel Queue Scheduling.

Unit-3: Memory Management: Logical & Physical Address Space, swapping, contiguous memory allocation, non-contiguous memory allocation paging and segmentation techniques, segmentation with paging; virtual memory management - Demand Paging & Page-Replacement Algorithms; Demand Segmentation.

Unit-4: File System: Different types of files and their access methods, directory structures, various allocation methods, disk scheduling and management and its associated algorithms, Introduction to distributed file system.

Unit-5: Process-Synchronization & Deadlocks: Critical Section Problems, semaphores; methods for handling deadlocks-deadlock prevention, avoidance & detection; deadlock recovery.

Unit-6: I/O Systems: I/O Hardware, Application I/O Interface, Kernel, Transforming I/O requests, Performance Issues.

Unit-7: Unix System And Windows NT Overview

Unix system call for processes and file system management, Shell interpreter, Windows NT architecture overview, Windows NT file system.

Text Books:

- Operating System Concepts by Silberchatz et al, 5th edition, 1998, Addison-Wesley.
- Modern Operating Systems by A. Tanenbaum, 1992, Prentice-Hall.
- Operating Systems Internals and Design Principles by William Stallings, 4th edition, 2001, Prentice-Hall

Reference Books:

- Operating System By Peterson , 1985, AW.
- Operating System By Milankovic, 1990, TMH.
- Operating System Incorporating With Unix & Windows By Colin Ritchie, 1974, TMH.
- Operating Systems by Mandrik & Donovan, TMH
- Operating Systems By Deitel, 1990, AWL.
- Operating Systems – Advanced Concepts By Mukesh Singhal , N.G. Shivaratri, 2003, T.M.H

Note: Eight questions will be set in all by the examiners taking at least one question from each unit. Students will be required to attempt five questions in all.

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CLASS WORK	:	50
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DURATION OF EXAM	:	3 HRS

PART A

UNIT1. THE 8085 PROCESSOR :

Introduction to microprocessor, 8085 microprocessor: Architecture, instruction set, interrupt structure, and assembly language programming.

UNIT2. THE 8086 MICROPROCESSOR ARCHITECTURE:

Architecture, block diagram of 8086, details of sub-blocks such as EU, BIU; memory segmentation and physical address computations, program relocation, addressing modes, instruction formats, pin diagram and description of various signals.

UNIT3. INSTRUCTION SET OF 8086:

Instruction execution timing, assembler instruction format, data transfer instructions, arithmetic instructions, branch instructions, looping instructions, NOP and HLT instructions, flag manipulation instructions, logical instructions, shift and rotate instructions, directives and operators, programming examples.

PART B

UNIT4. INTERFACING DEVICE :

The 8255 PPI chip: Architecture, control words, modes and examples.

UNIT 5. DMA :

Introduction to DMA process, 8237 DMA controller,

UNIT6. INTERRUPT AND TIMER :

8259 Programmable interrupt controller, Programmable interval timer chips.

TEXT BOOKS :

1. Microprocessor Architecture, Programming & Applications with 8085 : Ramesh S Gaonkar; Wiley Eastern Ltd.
2. The Intel Microprocessors 8086- Pentium processor : Brey; PHI

REFERENCE BOOKS:

1. Microprocessors and interfacing : Hall; TMH
2. The 8088 & 8086 Microprocessors-Programming, interfacing,Hardware & Applications :Triebel & Singh; PHI
3. Microcomputer systems: the 8086/8088 Family: architecture, Programming & Design : Yu-Chang Liu & Glenn A Gibson; PHI.
4. Advanced Microprocessors and Interfacing : Badri Ram; TMH

NOTE: 8 questions are to be set selecting FIVE questions from PART A and THREE questions from PART- B .Students have to attempt any five questions.

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Class Work: 50
Exam: 100
Total: 150
Duration of Exam: 3 Hrs.

Unit-1: Introduction to Computer Graphics: What is Computer Graphics, Computer Graphics Applications, Computer Graphics Hardware and software, Two dimensional Graphics Primitives: Points and Lines, Line drawing algorithms: DDA, Bresenham's; Circle drawing algorithms: Using polar coordinates, Bresenham's circle drawing, mid point circle drawing algorithm; Filled area algorithms: Scanline: Polygon filling algorithm, boundary filled algorithm.

Unit-2: Two/Three Dimensional Viewing: The 2-D viewing pipeline, windows, viewports, window to view port mapping; Clipping: point, clipping line (algorithms):- 4 bit code algorithm, Sutherland-cohen algorithm, parametric line clipping algorithm (Cyrus Beck).

Polygon clipping algorithm: Sutherland-Hodgeman polygon clipping algorithm. Two dimensional transformations: transformations, translation, scaling, rotation, reflection, composite transformation.

Three dimensional transformations: Three dimensional graphics concept, Matrix representation of 3-D Transformations, Composition of 3-D transformation.

Unit-3: Viewing in 3D: Projections, types of projections, the mathematics of planner geometric projections, coordinate systems.

Unit-4: Hidden surface removal: Introduction to hidden surface removal. The Z- buffer algorithm, scanline algorithm, area sub-division algorithm.

Unit-5: Representing Curves and Surfaces: Parametric representation of curves: Bezier curves, B-Spline curves. Parametric representation of surfaces; Interpolation method.

Unit-6: Illumination, shading, image manipulation: Illumination models, shading models for polygons, shadows, transparency. What is an image? Filtering, image processing, geometric transformation of images.

Text Books:

- Computer Graphics Principles and Practices second edition by James D. Foley, Andeies van Dam, Stevan K. Feiner and Johb F. Hughes, 2000, Addison Wesley.
- Computer Graphics by Donald Hearn and M.Pauline Baker, 2nd Edition, 1999, PHI

Reference Books:

- Procedural Elements for Computer Graphics – David F. Rogers, 2001, T.M.H Second Edition
- Fundamentals of 3Dimensional Computer Graphics by Alan Watt, 1999, Addison Wesley.
- Computer Graphics: Secrets and Solutions by Corrign John, BPB
- Graphics, GUI, Games & Multimedia Projects in C by Pilania & Mahendra, Standard Publ.
- Computer Graphics Secrets and solutions by Corrign John, 1994, BPV
- Introduction to Computer Graphics By N. Krishanmurthy T.M.H 2002

Note: Eight questions will be set in all by the examiners taking at least one question from each unit. Students will be required to attempt five questions in all.

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Class Work: 50
 Exam: 100
 Total: 150
 Duration of Exam: 3 Hrs.

Unit-1: Brief Review of Graphs, Sets and disjoint sets, union, sorting and searching algorithms and their analysis in terms of space and time complexity.

Unit-2: Divide and Conquer: General method, binary search, merge sort, quick sort, selection sort, Strassen's matrix multiplication algorithms and analysis of algorithms for these problems.

Unit-3: Greedy Method: General method, knapsack problem, job sequencing with dead lines, minimum spanning trees, single source paths and analysis of these problems.

Unit-4: Dynamic Programming: General method, optimal binary search trees, 0/1 knapsack, the traveling salesperson problem.

Unit-5: Back Tracking: General method, 8 queen's problem, graph colouring, Hamiltonian cycles, analysis of these problems.

Unit-6: Branch and Bound: Method, 0/1 knapsack and traveling salesperson problem, efficiency considerations. Techniques for algebraic problems, some lower bounds on parallel computations.

Unit-7: NP Hard and NP Complete Problems: Basic concepts, Cook's theorem, NP hard graph and NP scheduling problems some simplified NP hard problems.

Text Books:

- Fundamental of Computer algorithms, Ellis Horowitz and Sartaj Sahni, 1978, Galgotia Publ.,
- Introduction To Algorithms, Thomas H Cormen, Charles E Leiserson And Ronald L Rivest: 1990, TMH

Reference Books:

- The Design and Analysis of Computer Algorithm, Aho A.V. Hopcroft J.E., 1974, Addison Wesley.
- Algorithms-The Construction, Proof and Analysis of Programs, Berlion, P.Bizard, P., 1986. Johan Wiley & Sons,
- Writing Efficient Programs, Bentley, J.L., PHI
- Introduction to Design and Analysis of Algorithm, Goodman, S.E. & Hedetniemi, 1997, MGH.
- Introduction to Computers Science- An algorithms approach , Jean Paul Trembley, Richard B.Bunt, 2002, T.M.H.
- Fundamentals of Algorithms: The Art of Computer Programming Voll, Knuth, D.E.: 1985, Naresh Publ.

Note: Eight questions will be set in all by the examiners taking at least one question from each unit. Students will be required to attempt five questions in all.

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Class Work: 50
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 Duration of Exam: 3 Hrs.

Unit-1: Introduction to the Internet, The world wide web: The idea of hypertext and hyper media; How the web works-HTTP, HTML and URLs; How the browser works-MIME types, plugins and helper applications; The standards-HTML, XML, XHTML and the W3C.

Hypertext markup language: The anatomy of an HTML document; Marking up for structure and style: basic page markup, absolute and relative links, ordered and unordered lists, embedding images and controlling appearance, table creation and use, frames, nesting and targeting.

Descriptive markup: Meta tags for common tasks, semantic tags for aiding search, the doubling code and RDF.

Unit-2: Separating style from structure with style sheets: Internal style specifications within HTML, External linked style specification using CSS, page and site design considerations.

Client side programming: Introduction to the JavaScript syntax, the JavaScript object model, Event handling, Output in JavaScript, Forms handling, miscellaneous topics such as cookies, hidden fields, and images; Applications.

Unit-3: Server side programming: Introduction to Server Side Technologies CGI/ASP/JSP., Programming languages for server Side Scripting, Configuring the server to support CGI, applications; Input/ output operations on the WWW, Forms processing, (using PERL/VBSCRIPT/JavaScript)

Unit-4: Other dynamic content technologies: introduction to ASP & JSP, Delivering multimedia over web pages, The VRML idea, The Java phenomenon-applets and servlets, issues and web development.

Unit-5: Introduction to Microsoft .NET Technology and its comparison with the competing Technologies.

Text books:

- Beginning XHTML by Frank Boumperry, Cassandra Greer, Dave Raggett, Jenny Raggett, Sebastian Schnitzenbaumer & ted Wugofski, 2000, WROX press (Indian Shroff Publ. SPD) 1st edition
- HTML & XHTML: The Definitive Guide by Chuck Musciano, Bill Kennedy, 2000, 4th Edi.

Reference books:

- XHTML Black Book by Steven Holzner, 2000
- CGI Programming on the World Wide Web. O'Reilly Associates.
- Web Technologies By Achyut S Godbole , Atul Kahate, 2003, T.M.H
- Scott Guelich, Shishir Gundararam, Gunther Birzniek; CGI Programing with Perl 2/e O'Reilly.
- Doug Tidwell, James Snell, Pavel Kulchenko; Programming Web services, O'Reilly.
- Intranets by James D.Cimino, 1997, Jaico Publ.
- Internet and Web Technologies – Raj Kamal, 2002, T.M.H

Note: Eight questions will be set in all by the examiners taking at least one question from each unit. Students will be required to attempt five questions in all.

L	T	P
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Class Work: 50
 Exam: 100
 Total: 150
 Duration of Exam: 3 Hrs.

Unit-1: Basics of Multimedia Technology: Computers, communication and entertainment; multimedia an introduction; framework for multimedia systems; multimedia devices; CD- Audio, CD-ROM, CD-I, presentation devices and the user interface; multimedia presentation and authoring; professional development tools; LANs and multimedia; internet, World Wide Web & multimedia distribution network-ATM & ADSL; multimedia servers & databases; vector graphics; 3D graphics programs; animation techniques; shading; anti aliasing; morphing; video on demand.

Unit-2: Image Compression & Standards: Making still images; editing and capturing images; scanning images; computer color models; color palettes; vector drawing; 3D drawing and rendering; JPEG-objectives and architecture; JPEG-DCT encoding and quantization, JPEG statistical coding, JPEG predictive lossless coding; JPEG performance; overview of other image file formats as GIF, TIFF, BMP, PNG etc.

Unit-3: Audio & Video: Digital representation of sound; time domain sampled representation; method of encoding the analog signals; subband coding; fourier method; transmission of digital sound; digital audio signal processing; stereophonic & quadraphonic signal processing; editing sampled sound; MPEG Audio; audio compression & decompression; brief survey of speech recognition and generation; audio synthesis; musical instrument digital interface; digital video and image compression; MPEG motion video compression standard; DVI technology; time base media representation and delivery.

Unit-4: Virtual Reality: Applications of multimedia, intelligent multimedia system, desktop virtual reality, VR operating system, virtual environment displays and orientation making; visually coupled system requirements; intelligent VR software systems. Applications of environment in various fields.

Text Books:

- An introduction, Villamil & Molina, Multimedia Mc Milan, 1997
- multimedia: Sound & Video, Lozano, 1997, PHI, (Que)

Reference Books:

- Multimedia: Production, planning and delivery, Villamil & Molina, Que, 1997
- Multimedia on the PC, Sinclair, BPB
- Multimedia: Making it work, Tay Vaughan, fifth edition, 1994, TMH.
- Multimedia in Action by James E Shuman, 1997, Wadsworth Publ.,
- Multimedia in Practice by Jeff coate Judith, 1995, PHI.
- Multimedia Systems by Koegel, AWL
- Multimedia Making it Work by Vaughar, etl.
- Multimedia Systems by John .F. Koegel, 2001, Buford.
- Multimedia Communications by Halsall & Fred, 2001, AW.

Note: Eight questions will be set in all by the examiners taking at least one question from each unit. Students will be required to attempt five questions in all.

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Class Work:	50
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Total:	100
Duration of Exam:	3 Hrs.

List of programs to be developed

1. Write a program for 2D line drawing as Raster Graphics Display.
2. Write a program for circle drawing as Raster Graphics Display.
3. Write a program for polygon filling as Raster Graphics Display
4. Write a program for line clipping.
5. Write a program for polygon clipping.
6. Write a program for displaying 3D objects as 2D display using perspective transformation.
7. Write a program for rotation of a 3D object about arbitrary axis.
8. Write a program for Hidden surface removal from a 3D object.

Note: At least 5 to 10 more exercises to be given by the teacher concerned.

CSE-311 E**Web Development & Core JAVA Lab.**

L	T	P
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Class Work:	25
Exam:	25
Total:	50
Duration of Exam:	3 Hrs.

Java programs using classes & objects and various control constructs such as loops etc , and data structures such as arrays , structures and functions.

Java programs for creating Applets for display of Images ,Texts and Animation

Programs related to interfaces & packages

Input output & Random files programs in java

Java programs using Event driven concept

Programs related to Network Programming

Development of Web site for the college or newspaper agency.

Books recommended for Lab.

- Java Elements – Principles of Programming in Java , Duane A. Bailey , Duane W. Bailey, 2000, T.M.H
- The Java Handbook by Patrick Naughton, TMH, N.Delhi

L T P
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Class Work: 25
Exam: 25
Total: 50
Duration of Exam: 3 Hrs.

1. Write a program to justify a text entered by the user on both the left and right hand side. For example, the text “ An architect may have a graphics program to draw an entire building but be interested in only ground floor”, can be justified in 30 columns as shown below. An architect may have a Graphics programs draw an Entric building but be interested in only ground floor.
2. Study the notes of a piano and stimulate them using the key board and store them in a file.
3. Write a program to read a paragraph and store it to a file name suggested by the author.
4. Devise a routine to produce the animation effect of a square transforming to a triangle and then to a circle.
5. Write a program to show a bitmap image on your computer screen.
6. Create a web page for a clothing company which contains all the details of that company and at-least five links to other web pages.
7. Write a program by which we can split mpeg video into smaller pieces for the purpose of sending it over the web or by small capacity floppy diskettes and then joining them at the destination.
8. Write a program to simulate the game of pool table.
9. Write a program to simulate the game Mine Sweeper.
10. Write a program to play “wave” or “midi” format sound files.

Note: At least 5 to 10 more exercises to be given by the teacher concerned.

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CLASS WORK	:	25
EXAM	:	25
TOTAL	:	50
DURATION OF EXAM	:	3 HRS

LIST OF EXPERIMENTS:

1. Study of 8085 Microprocessor kit.
2. Write a program using 8085 and verify for :
 - a. Addition of two 8-bit numbers.
 - b. Addition of two 8-bit numbers (with carry).
3. Write a program using 8085 and verify for :
 - a. 8-bit subtraction (display borrow)
 - b. 16-bit subtraction (display borrow)
4. Write a program using 8085 for multiplication of two 8- bit numbers by repeated addition method. Check for minimum number of additions and test for typical data.
5. Write a program using 8085 for multiplication of two 8- bit numbers by bit rotation method and verify.
6. Write a program using 8085 for division of two 8- bit numbers by repeated subtraction method and test for typical data.
7. Write a program using 8085 for dividing two 8- bit numbers by bit rotation method and test for typical data.
8. Study of 8086 microprocessor kit
9. Write a program using 8086 for division of a defined double word (stored in a data segment) by another double Word division and verify.
10. Write a program using 8086 for finding the square root of a given number and verify.
11. Write a program using 8086 for copying 12 bytes of data from source to destination and verify.
12. Write a program using 8086 and verify for:
 - a. Finding the largest number from an array.
 - b. Finding the smallest number from an array.
13. Write a program using 8086 for arranging an array of numbers in descending order and verify.
14. Write a program using 8086 for arranging an array of numbers in ascending order and verify.
15. Write a program for finding square of a number using look-up table and verify. .

16. Write a program to interface a two digit number using seven-segment LEDs. Use 8085/8086 microprocessor and 8255 PPI.

17. Write a program to control the operation of stepper motor using 8085/8086 microprocessor and 8255 PPI.

NOTE: At least ten experiments have to be performed in the semester out of which seven experiments should be performed from above list. Remaining three experiments may either be performed from the above list or designed & set by the concerned institution as per the scope of the syllabus of EE-309-C.

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Class Work:	25
Exam:	25
Total:	50
Duration of Exam: 3 Hrs.	

- Study of WINDOWS 2000 Operating System.
- Administration of WINDOWS 2000 (including DNS,LDAP, Directory Services)
- Study of LINUX Operating System (Linux kernel, shell, basic commands pipe & filter commands).
- Administration of LINUX Operating System.
- Writing of Shell Scripts (Shell programming).
- AWK programming.

CSE-302 E**Principles of Software Engineering**

L	T	P
3	1	–

Class Work: 50

Exam: 100

Total: 150

Duration of Exam: 3 Hrs.

Unit-1: Introduction: The process, software products, emergence of software engineering, evolving role of software, software life cycle models, Software Characteristics, Applications, Software crisis.

Unit-2 : Software project management: Project management concepts, software process and project metrics Project planning, project size estimation metrics, project estimation Techniques, empirical estimation techniques, COCOMO- A Heuristic estimation techniques, staffing level estimation, team structures, staffing, risk analysis and management, project scheduling and tracking.

Unit-3 : Requirements Analysis and specification requirements engineering, system modeling and simulation Analysis principles modeling, partitioning Software, prototyping: , Prototyping methods and tools; Specification principles, Representation, the software requirements specification and reviews Analysis Modeling: Data Modeling, Functional modeling and information flow: Data flow diagrams, Behavioral Modeling; The mechanics of structured analysis: Creating entity/ relationship diagram, data flow model, control flow model, the control and process specification; The data dictionary; Other classical analysis methods.

Unit-4: System Design: Design concepts and principles: the design process: Design and software quality, design principles; Design concepts: Abstraction, refinement, modularity, software architecture, control hierarchy, structural partitioning, data structure, software procedure, information hiding; Effective modular design: Functional independence, Cohesion, Coupling; Design Heuristics for effective modularity; The design model; Design documentation.

Architectural Design: Software architecture, Data Design: Data modeling, data structures, databases and the data warehouse, Analyzing alternative Architectural Designs ,architectural complexity; Mapping requirements into a software architecture; Transform flow, Transaction flow; Transform mapping: Refining the architectural design.

Unit-5 : Testing and maintenance: Software Testing Techniques, software testing fundamentals: objectives, principles, testability; Test case design, white box testing, basis path testing; Control structure testing: Black box testing, testing for specialized environments ,architectures and applications. Software Testing Strategies: Verification and validation, Unit testing, Integration testing,; Validation testing, alpha and beta testing; System testing: Recovery testing, security testing, stress testing, performance testing; The art of debugging, the debugging process debugging approaches. Software re-engineering , reverse engineering ,restructuring, forward engineering.

Unit-6 : Software Reliability and Quality Assurance :Quality concepts, Software quality assurance , SQA activities; Software reviews: cost impact of software defects, defect amplification and removal; formal technical reviews: The review meeting, review reporting and record keeping, review guidelines; Formal approaches to SQA; Statistical software quality assurance; software reliability: Measures of reliability and availability ,The ISO 9000 Quality standards: The ISO approach to quality assurance systems, The ISO 9001 standard, Software Configuration Management.

Unit-7: Computer Aided software Engineering: CASE, building blocks, integrated case environments and architecture, repository.

Text Book:

- Software Engineering – A Practitioner’s Approach, Roger S. Pressman, 1996, MGH.

Reference Books:

- Fundamentals of software Engineering, Rajib Mall, PHI
- Software Engineering by Ian Sommerville, Pearson Edu, 5th edition, 1999, AW,
- Software Engineering – David Gustafson, 2002, T.M.H
- Software Engineering Fundamentals Oxford University, Ali Behforooz and Frederick J. Hudson 1995 JW&S,
- An Integrated Approach to software engineering by Pankaj Jalote , 1991 Narosa,

Note: Eight questions will be set in all by the examiners taking at least one question from each unit. Students will be required to attempt five questions in all.

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Class Work:	50
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Duration of Exam: 3 Hrs.	

Unit-1: Foundational issues in intelligent systems: Foundation and history of AI, AI problems and techniques – AI programming languages, introduction to LISP and PROLOG- problem spaces and searches, blind search strategies, Breadth first- Depth first- heuristic search techniques Hill climbing: best first- A * algorithm AO* algorithm- game tree, Min max algorithms, game playing- alpha beta pruning.

Unit-2: Knowledge representation issues, predicate logic- logic programming, semantic nets- frames and inheritance, constraint propagation, representing knowledge using rules, rules based deduction systems.

Unit-3: Reasoning under uncertainty, review of probability, Baye's probabilistic interferences and Dempster shafer theory, Heuristic methods, symbolic reasoning under uncertainty, Statistical reasoning, Fuzzy reasoning, Temporal reasoning, Non monotonic reasoning.

Unit-4: Planning, planning in situational calculus, representation for planning, partial order planning algorithm, learning from examples, discovery as learning, Learning by analogy, explanation based learning, neural nets, genetic algorithms.

Unit-5: Principles of Natural language processing, rule based systems architecture, Expert systems, knowledge acquisition concepts, AI application to robotics, and current trends in intelligent systems.

Text Book:

- Artificial Intelligence: A Modern Approach,. Russell & Norvig. 1995, Prentice Hall.

Reference Books:

- Artificial Intelligence, Elaine Rich and Kevin Knight, 1991, TMH.
- Artificial Intelligence-A modern approach, Stuart Russel and Peter Norvig, 1998, PHI.
- Artificial intelligence, Patrick Henry Winston:, 1992, Addison Wesley 3rd Ed.,

Note: Eight questions will be set in all by the examiners taking at least one question from each unit. Students will be required to attempt five questions in all.

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Class Work:	50
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Duration of Exam: 3 Hrs.	

Unit-1: OSI Reference Model and Network Architecture: Introduction to Computer Networks, Example networks ARPANET, Internet, Private Networks, Network Topologies: Bus-, Star-, Ring-, Hybrid -, Tree -, Complete -, Irregular –Topology; Types of Networks : Local Area Networks, Metropolitan Area Networks, Wide Area Networks; Layering architecture of networks, OSI model, Functions of each layer, Services and Protocols of each layer

Unit-2: TCP/IP: Introduction, History of TCP/IP, Layers of TCP/IP, Protocols, Internet Protocol, Transmission Control Protocol, User Datagram Protocol, IP Addressing, IP address classes, Subnet Addressing, Internet Control Protocols, ARP, RARP, ICMP, Application Layer, Domain Name System, Email – SMTP, POP,IMAP; FTP, NNTP, HTTP, Overview of IP version 6.

Unit-3: Local Area Networks: Introduction to LANs, Features of LANs, Components of LANs, Usage of LANs, LAN Standards, IEEE 802 standards, Channel Access Methods, Aloha, CSMA, CSMA/CD, Token Passing, Ethernet, Layer 2 & 3 switching, Fast Ethernet and Gigabit Ethernet, Token Ring, LAN interconnecting devices: Hubs, Switches, Bridges, Routers, Gateways.

Unit-4: Wide Area Networks: Introduction of WANs, Routing, Congestion Control, WAN Technologies, Distributed Queue Dual Bus (DQDB), Synchronous Digital Hierarchy (SDH)/ Synchronous Optical Network (SONET), Asynchronous Transfer Mode (ATM), Frame Relay, Wireless Links.

Unit-5: Introduction to Network Management: Remote Monitoring Techniques: Polling, Traps, Performance Management, Class of Service, Quality of Service, Security management, Firewalls, VLANs, Proxy Servers, Introduction to Network Operating Systems: Client-Server infrastructure, Windows NT/2000.

Text Book:

- Computer Networks (3rd edition), Tanenbaum Andrew S., International edition, 1996.

Reference Books:

- Data Communications, Computer Networks and Open Systems (4th edition), Halsall Fred, 2000, Addison Wesley, Low Price Edition.
- Business Data Communications, Fitzgerald Jerry,.
- Computer Networks – A System Approach, Larry L. Peterson & Bruce S. Davie, 2nd Edition
- Computer Networking – ED Tittel, 2002, T.M.H.

Note: Eight questions will be set in all by the examiners taking at least one question from each unit. Students will be required to attempt five questions in all.

IT-303 E Systems Programming & System Administration

L	T	P
3	1	–

Class Work:	50
Exam:	100
Total:	150
Duration of Exam:	3 Hrs.

Unit-1: Evolution of Components Systems Programming, Assemblers, Loaders, Linkers, Macros, Compilers. software tools, Text editors, Interpreters and program generators, Debug Monitors, Programming environment.

Unit-2: Compiler: Brief overview of compilation process, Incremental compiler, Assembler: Problem statement, single phase and two phase assembler, symbol table; Loader schemes, compile and go Loader, general loader schemes, absolute loader, Subroutine linkage, Reallocating loader, Direct linkage Loader, Binders, Linking loader, overlays.

Unit-3: Macro language and macro-processor, macro instructions, features of macro facility, macro instruction arguments, conditional macro expansion, macro calls with macro instruction defining macros.

Unit-4: Theoretical Concept of Unix Operating System: Basic features of operating system; File structure: CPU scheduling; Memory management: swapping, demand paging; file system: block

and fragments, inodes, directory structure; User to user communication.

Unit-5: Getting Started with Unix: User names and groups, logging in; Format of Unix commands; Changing your password; Characters with special meaning; Unix documentation; Files

and directories; Current directory, looking at the directory contents, absolute and relative pathnames,

some Unix directories and files; Looking at the file contents; File permissions; basic operation on files; changing permission modes; Standard files, standard output; Standard input, standard error; filters and pipelines; Processes; finding out about processes; Stopping background process; Unix editor vi.

Unit-6: Test Manipulation: Inspecting files; File statistics; Searching for patterns; Comparing files; Operating on files; Printing files; Rearranging files; Sorting files; Splitting files; Translating characters; AWK utility.

Unit-7: Shell Programming: Programming in the Borne and C-Shell; Wild cards; Simple shell programs; Shell variables; Shell programming constructs; interactive shell scripts; Advanced features.

Unit-8: System Administration: Definition of system administration; Booting the system; Maintaining user accounts; File systems and special files; Backups and restoration; Role and functions of a system manager.

Overview of the linux. operating system

Text Books:

- Systems Programming by Donovan, TMH.
- The unix programming environment by Brain Kernighen & Rob Pike, 1984, PHI & Rob Pike.
- Design of the Unix operating system by Maurich Bach, 1986, PHI.
- Introduction to UNIX and LINUX by John Muster, 2003, TMH.

Reference Book:

- Advanced Unix programmer's Guide by Stephen Prato, BPB
- Unix- Concept and applications by Sumitabha Das, 2002, T.M..H

Note: Eight questions will be set in all by the examiners taking at least one question from each unit. Students will be required to attempt five questions in all.

EE-402-E**WIRELESS COMMUNICATION**

L T P
3 1 0

CLASS WORK	:	50
EXAM	:	100
TOTAL	:	150
DURATION OF EXAM	:	3 HRS

UNIT 1. INTRODUCTION TO WIRELESS COMMUNICATION SYSTEMS:

Evolution of mobile radio communications, examples of wireless comm. systems, paging systems, Cordless telephone systems, comparison of various wireless systems.

UNIT 2. MODERN WIRELESS COMMUNICATION SYSTEMS:

Second generation cellular networks, third generation wireless networks, wireless in local loop, wireless local area networks, Blue tooth and Personal Area networks.

UNIT 3. INTRODUCTION TO CELLULAR MOBILE SYSTEMS:

Spectrum Allocation, basic Cellular Systems, performance Criteria, Operation of cellular systems, analog cellular systems, digital Cellular Systems.

UNIT 4. CELLULAR SYSTEM DESIGN FUNDAMENTALS:

Frequency Reuse, channel assignment strategies, handoff Strategies, Interference and system capacity, tracking and grade off service, improving coverage and capacity.

UNIT 5. MULTIPLE ACCESS TECHNIQUES FOR WIRELESS COMMUNICATION:

Introduction to Multiple Access, FDMA, TDMA, Spread Spectrum multiple Access, space division multiple access, packet ratio, capacity of a cellular systems.

UNIT 6. WIRELESS NETWORKING:

Difference between wireless and fixed telephone networks, development of wireless networks, fixed network transmission hierarchy, traffic routing in wireless networks, wireless data services, common channel signaling, ISDN (Integrated Services digital Networks), advanced intelligent networks.

UNIT 7. INTELLIGENT CELL CONCEPT AND APPLICATION:

Intelligent cell concept, applications of intelligent micro-cell Systems, in-Building Communication, CDMA cellular Radio Networks.

TEXT BOOKS:

1. Wireless Communications: Theodore S. Rappaport; Pearsons.
2. Mobile Cellular Telecommunication: W.C.Y.Lee; McGraw Hill

REFERENCE BOOK:

1. Mobile Communications: Jochen Schiller; Pearson

NOTE: Eight questions are to be set -one question from each unit. Students have to attempt any five question.

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CLASS WORK	:	50
EXAM	:	100
TOTAL	:	150
DURATION OF EXAM	:	3 HRS

UNIT 1. INTRODUCTION :

Introduction to Computer-aided design tools for digital systems. Hardware description languages; introduction to VHDL, data objects, classes and data types, Operators, Overloading, logical operators. Types of delays Entity and Architecture declaration. Introduction to behavioural, dataflow and structural models.

UNIT 2. VHDL STATEMENTS :

Assignment statements, sequential statements and process, conditional statements, case statement Array and loops, resolution functions, Packages and Libraries, concurrent statements. Subprograms: Application of Functions and Procedures, Structural Modelling, component declaration, structural layout and generics.

UNIT 3. COMBINATIONAL CIRCUIT DESIGN:

VHDL Models and Simulation of combinational circuits such as Multiplexers, Demultiplexers, encoders, decoders, code converters, comparators, implementation of Boolean functions etc.

UNIT 4. SEQUENTIAL CIRCUITS DESIGN :

VHDL Models and Simulation of Sequential Circuits
Shift Registers, Counters etc.

UNIT 5. DESIGN OF MICROCOMPUTER :

Basic components of a computer, specifications, architecture of a simple microcomputer system, implementation of a simple microcomputer system using VHDL

UNIT 6. DESIGN WITH CPLDs AND FPGAs :

Programmable logic devices : ROM, PLAs, PALs, GAL, PEEL, CPLDs and FPGA. Design implementation using CPLDs and FPGAs

REFERENCE BOOKS:

1. IEEE Standard VHDL Language Reference Manual (1993).
2. Digital Design and Modelling with VHDL and Synthesis : KC Chang; IEEE Computer Society Press.
3. "A VHDL Primer" : Bhasker; Prentice Hall 1995.
4. "Digital System Design using VHDL" : Charles. H.Roth ; PWS (1998).
5. "VHDL-Analysis & Modelling of Digital Systems" : Navabi Z; McGraw Hill.
6. VHDL-IV Edition :Perry; TMH (2002)
7. "Introduction to Digital Systems" : Ercegovic. Lang & Moreno; John Wiley (1999).
8. Fundamentals of Digital Logic with VHDL Design : Brown and Vranesic; TMH (2000)
9. Modern Digital Electronics- III Edition: R.P Jain; TMH (2003).

NOTE : Eight questions are to be set - at least one question from each unit. Students will be required to attempt five questions in all.

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Class Work:	25
Exam:	25
Total:	50
Duration of Exam:	3 Hrs.

1. Study of PROLOG.

Write the following programs using PROLOG.

2. Write a program to solve 8 queens problem.
3. Solve any problem using depth first search.
4. Solve any problem using best first search.
5. Solve 8-puzzle problem using best first search
6. Solve Robot (traversal) problem using means End Analysis.
7. Solve traveling salesman problem.

Note: At least 5 to 10 more exercises to be given by the teacher concerned.

DIGITAL SYSTEM DESIGN LAB

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CLASS WORK	:	25
EXAM	:	25
TOTAL	:	50
DURATION OF EXAM	:	3 HRS

LIST OF EXPERIMENTS:

1. Design all gates using VHDL.
2. Write VHDL programs for the following circuits, check the wave forms and the hardware generated
 - a. half adder
 - b. full adder
3. Write VHDL programs for the following circuits, check the wave forms and the hardware generated
 - a. multiplexer
 - b. demultiplexer
4. Write VHDL programs for the following circuits, check the wave forms and the hardware generated
 - a. decoder
 - b. encoder
5. Write a VHDL program for a comparator and check the wave forms and the hardware generated
- 6 Write a VHDL program for a code converter and check the wave forms and the hardware generated
7. Write a VHDL program for a FLIP-FLOP and check the wave forms and the hardware generated
8. Write a VHDL program for a counter and check the wave forms and the hardware generated
9. Write VHDL programs for the following circuits, check the wave forms and the hardware generated
 - a. register
 - b. shift register
10. Implement any three (given above) on FPGA/CPLD kit

NOTE : Ten experiments are to be performed out of which at least seven experiments should be performed from above list. Remaining three experiments may either be performed from the above list or designed & set by the concerned institution as per the scope of the syllabus.